

Electronic Acknowledgement Receipt

EFS ID:	1334841
Application Number:	10628896
International Application Number:	
Confirmation Number:	7428
Title of Invention:	SYSTEM AND METHOD FOR PROVIDING A REDUNDANT MEMORY ARRAY IN A SEMICONDUCTOR MEMORY INTEGRATED CIRCUIT
First Named Inventor/Applicant Name:	Chao-Wu Chen
Customer Number:	32605
Filer:	Edward Chungfan Kwok/Jeannie Ha
Filer Authorized By:	Edward Chungfan Kwok
Attorney Docket Number:	M-15049 US
Receipt Date:	27-NOV-2006
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Time Stamp:	16:05:30
Application Type:	Utility

Payment information:

Submitted with Payment	yes
Payment was successfully received in RAM	\$ 1009
RAM confirmation Number	265
Deposit Account	502257

The Director of the USPTO is hereby authorized to charge indicated fees and credit any overpayment as follows:
Charge any Additional Fees required under 37 C.F.R. Section 1.16 and 1.17

File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Issue Fee Payment Recorded	DOC112706.pdf	181197	no	1

Warnings:

Information:

2	Fee Worksheet (PTO-875)	fee-info.pdf	8519	no	2
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Warnings:

Information:

Total Files Size (in bytes):	189716
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New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.